	Application No.	Applicant(s)
Notice of Allowability	10/621,576 Examiner	YOO ET AL. Art Unit
	Stephen W. Smoot	2813
The MAILING DATE of this communication appr All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	plication. If not included n will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>applicant's amendment</u>	ent filed on 14 June 2006.	
2. The allowed claim(s) is/are 1-13,18 and 22.		
 3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application No	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv		
 5. CORRECTED DRAWINGS (as "replacement sheets") mure (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date (lateral p	son's Patent Drawing Review (PTO s Amendment / Comment or in the C	Office action of ngs in the front (not the back) of d). must be submitted. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Da 08), 7. ☒ Examiner's Amend	te .
Stephen	I. Smoot	Stephen W. Smoot Patent Examiner Art Unit 2813

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This Office action is in response to applicant's amendment filed on 14 June 2006, which has been entered.

EXAMINER'S AMENDMENT

- 1. An examiner's amendment to the record appears below to correct obvious informalities in claim 18. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 2. The application has been amended as follows:

In the Claims:

In claim 18, line 1, delete "14. (Currently Amended)"; and In claim 18, last line, change "circuit" to --circuit board--.

- 3. Claims 1-13, 18, 22 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

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- Claims 1-13 are allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, a method of assembling a semiconductor package that includes the steps of attaching a protective tape to the active surface of a semiconductor wafer, sawing the semiconductor wafer to separate it into a plurality of individual chips while each individual chip remains covered by a portion of the protective tape, attaching an individual chip to a chip pad, and subsequently removing the portion of the protective tape from the individual chip;
- Claim 18 is allowed because the prior art of record does not teach or suggest, in
 combination with the other claim limitations, an apparatus for use in assembling
 a semiconductor package that includes a chip positioning device, a tape
 positioning device and a tape displacement device, wherein the chip positioning
 device is arranged and configured to hold a plurality of individual chips mounted
 on a frame, a leadframe, or a circuit board; and
- Claim 22 is allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, an apparatus for use in assembling a semiconductor package that includes a chip positioning device and a tape removal device, wherein the chip positioning device is arranged and configured to hold a plurality of individual chips mounted on a frame, a leadframe, or a circuit board.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on

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Statement of Reasons for Allowance."

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen W. Smoot whose telephone number is 571-

272-1698. The examiner can normally be reached on M-F (8:00 am to 4:30 pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on 571-272-1702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.